



SSC8L412GT8

N-Channel Enhancement Mode MOSFET

➤ Features

V_{DS}	V_{GS}	$R_{DS(ON)}$ Typ.	I_D
40V	$\pm 20V$	1.7 m Ω @10V	204A
		2.5 m Ω @4.5V	

➤ Description

This device is N-Channel enhancement MOSFET. Uses SGT technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit.

100% UIS + ΔV_{DS} + R_g Tested!

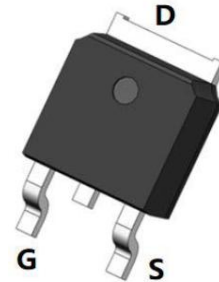
➤ Applications

- DC/DC converters
- Power supplies
- Motor Drive Control
- Synchronous rectification

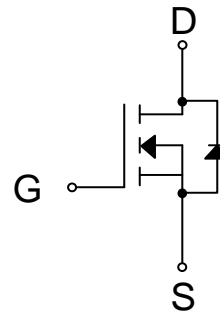
➤ Ordering Information

Device	Package	Shipping
SSC8L412GT8	TO-252-2L	2500/Reel

➤ Pin Configuration



TO-252-2L (Top View)



Pin Configuration



Marking

(XXYY: Internal Traceability Code)

**➤ Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)**

Symbol	Parameter		Ratings	Unit
V_{DSS}	Drain-to-Source Voltage		40	V
V_{GSS}	Gate-to-Source Voltage		± 20	V
I_D	Continuous Drain Current ^d	$T_C=25^{\circ}\text{C}$	204	A
		$T_C=100^{\circ}\text{C}$	113	
I_{DSM}	Continuous Drain Current ^a	$T_A=25^{\circ}\text{C}$	34	A
		$T_A=70^{\circ}\text{C}$	25	
I_{DM}	Pulsed Drain Current ^b		816	A
P_D	Power Dissipation ^c	$T_C=25^{\circ}\text{C}$	113	W
		$T_C=100^{\circ}\text{C}$	45	
P_{DSM}	Power Dissipation ^a	$T_A=25^{\circ}\text{C}$	3.3	W
		$T_A=70^{\circ}\text{C}$	2.1	
I_{AS}	Avalanche Current ^b $L=0.5\text{mH}$ Single Pulse		35	A
E_{AS}	Avalanche Energy ^b $L=0.5\text{mH}$ Single Pulse		306	mJ
T_J	Operation junction temperature		-55~150	$^{\circ}\text{C}$
T_{STG}	Storage temperature range		-55~150	

➤ Thermal Resistance Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ^a	38	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	1.1	

Note:

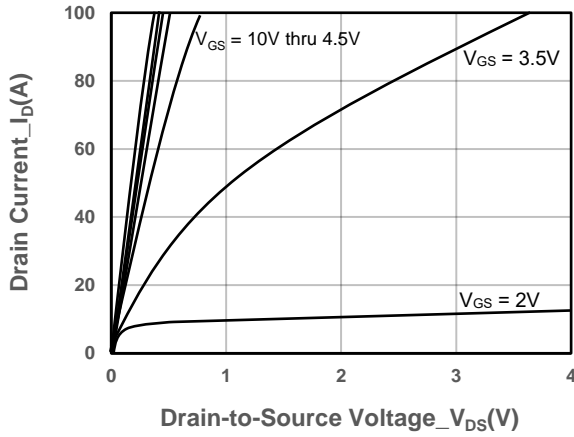
- The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The value in any given application depends on the user is specific board design. The power dissipation is based on the $t \leq 10\text{s}$ thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation P_D is based on $T_{J(MAX)}=150^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- The maximum current rating is package limited.

**➤ Electrical Characteristics (T_A=25°C unless otherwise noted)**

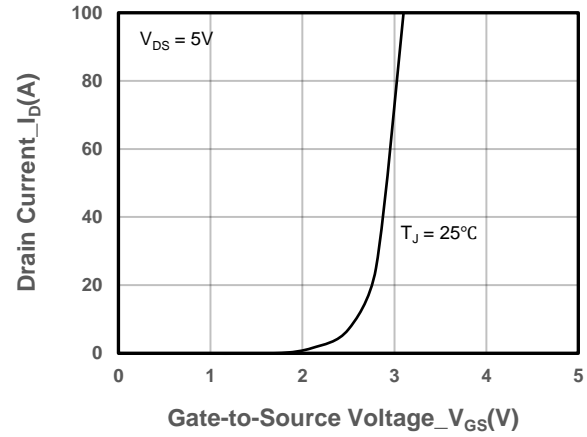
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	40			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250uA	1.0	1.8	2.5	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 20A		1.7	2.5	mΩ
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 4.5V, I _D = 10A		2.5	3.5	mΩ
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 40V, V _{GS} = 0V			1	μA
Gate-Source Leak Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
Transconductance	G _{FS}	V _{DS} = 5V, I _D = 10A		30		S
Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = 10A		0.7	1.3	V
Gate Resistance	R _G	V _{DS} = 0V, f = 1MHz		2.4		Ω
Input Capacitance	C _{ISS}	V _{DS} = 20V, V _{GS} = 0V, f = 1MHz		3243		pF
Output Capacitance	C _{OSS}			1304		
Reverse Transfer Capacitance	C _{RSS}			94		
Total Gate Charge	Q _G	V _{GS} = 10V, V _{DS} = 20V, I _D = 20A		49		nC
Gate to Source Charge	Q _{GS}			11		
Gate to Drain Charge	Q _{GD}			7.5		
Turn-on Delay Time	T _{D(ON)}	V _{GS} = 10V, V _{DS} = 20V, I _D = 20A, R _G = 3Ω		8.4		ns
Rise Time	T _r			8.7		
Turn-off Delay Time	T _{D(OFF)}			44		
Fall Time	T _f			26		
Diode Recovery Time	T _{rr}	I _F =20A, di/dt=100A/us		53		ns
Diode Recovery Charge	Q _{rr}	I _F =20A, di/dt=100A/us		39		nC



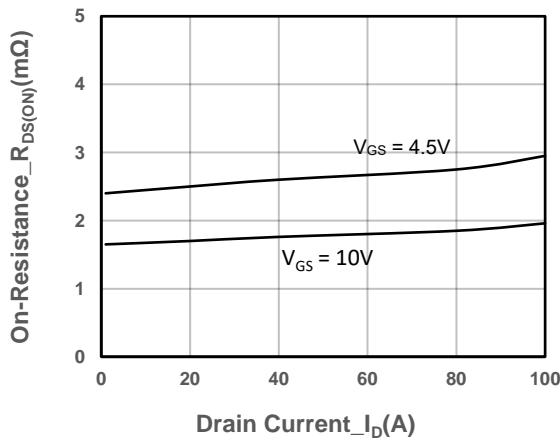
➤ Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)



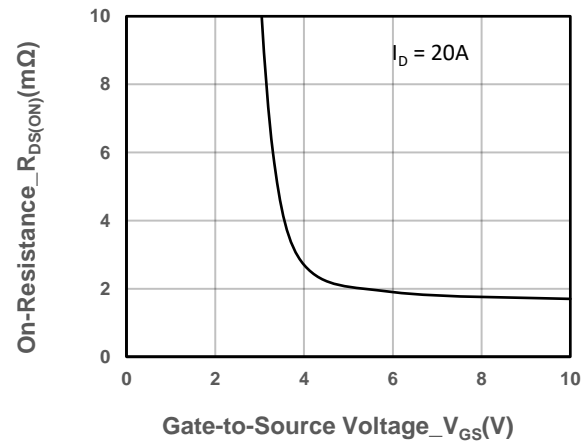
Output Characteristics



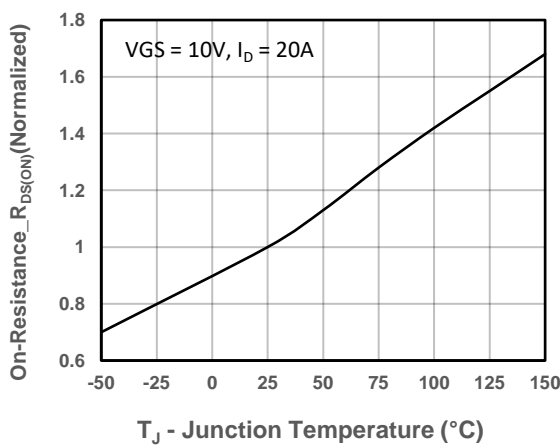
Transfer Characteristics



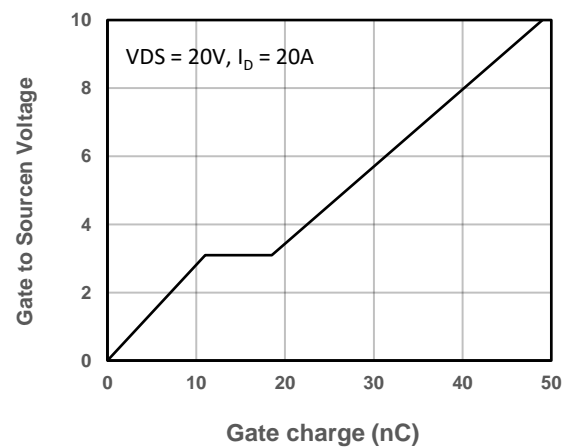
On-Resistance vs. Drain Current and Gate Voltage



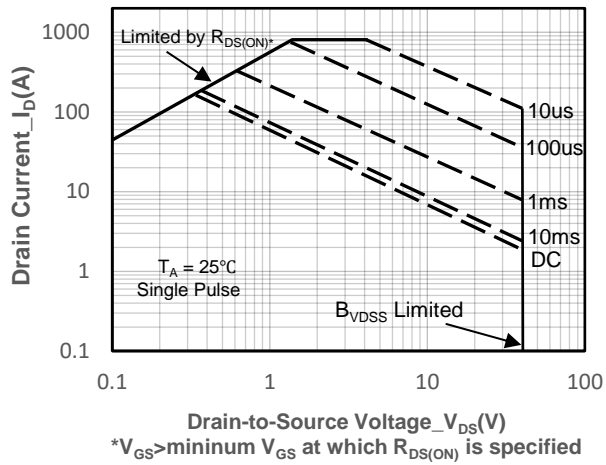
On-Resistance vs. Gate-to-Source Voltage



On-Resistance vs. Junction Temperature

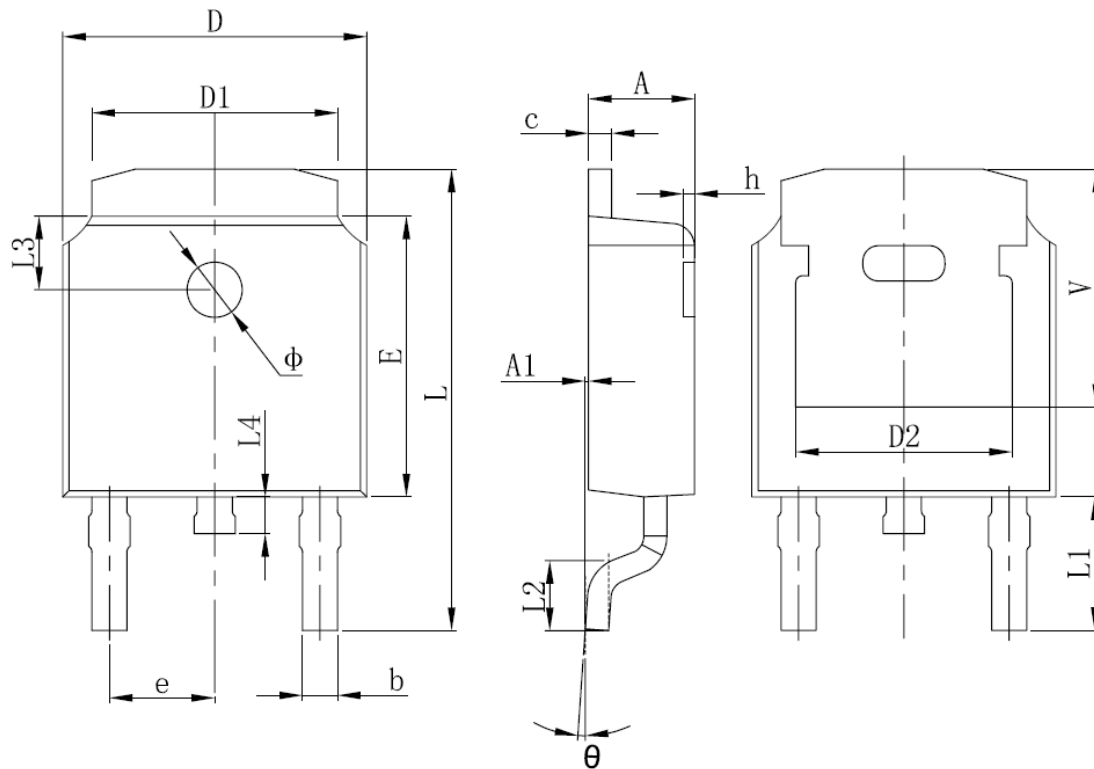


Gate-Source Voltage vs. Gate charge



Safe Operating Area vs. Junction-to-Ambient

➤ Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 REF.		0.190 REF.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900 REF.		0.114 REF.	
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063 REF.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250 REF.		0.207 REF.	



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